OEM 56K DSU
56 Kbps DSU/CSU with TTL Interface for OEM Applications

FEATURES
- 56 Kbps
- TTL interface
- DCB will customize
- Synchronous
- Point-to-point or multipoint
- DDS service or line driver mode
- Responds to Bell defined current reversal loopback and non-latching loopback
- Exceeds Telco RX attenuation specifications by more than 10 dB

DESCRIPTION
The DCB 56 Kbps OEM DSU/CSU is a 56 Kbps synchronous DSU/CSU for use with DDS or equivalent digital services. The DL-56 complies with Bell system encoding, decoding, timing, interface and diagnostic standards. It will operate point-to-point or multipoint. The DL-56 has a master clock mode that allows it to be used in line driver mode over customer owned cable.

The DL-56 OEM DSU is easy to implement. It requires a simple 5 volt DC power supply. The OEM board has TTL logic output for the data and control leads. It also includes complete indicators and the controls necessary to control clocking, loopbacks, and point-to-point or multipoint operation. For a moderate fee, typically ranging from $5,000 to $10,000, the OEM DSU board can be provided in custom form factors. The board can be as small as 25 square inches. DCB will manufacture the unit or will license manufacturing.

The DL-56 uses the latest line interface technology, which exceeds digital services attenuation specifications by more than 10 dB. This translates into longer cable distances from the phone company central office to the customer premise, or longer customer owned cable runs for line driver applications.

The DL-56 is an ideal choice for any application requiring 56 Kbps digital service, including bridging, multiplexing and multidrop lines.
SPECIFICATIONS

General
Rates: 56 Kbps synchronous
Application: Point-to-point or multipoint
Facility: Dataphone Digital Service (DDS) or equivalent, Line driver mode for customer owned 4-wire circuit

Indicators (front panel)
Transmit Data, Receive Data, Request to Send, Clear to Send, Data Carrier Detect, Test

Controls
DIP switches for Bidirectional Loopback, master or slave clock, RTS controlled by terminal or forced on.
Responds to telco current reversal loopback and non-latching pattern loopback.

Telco
Interface: DDS bipolar return to zero
Connector: 8-wire RJ-48S

Data Port
Connector: 14-Pin, 2 rows by 7pins on 1/10" centers
Interface: TTL
Timing: Synchronous

Physical/Electrical
Power requirements: 5 volts DC
9" x 5" printed circuit board
The DSU components can fit in as few as 25 square inches
Custom board sizes and layout are available.
Call DCB for more information.